

### **Amendments to the Specification:**

Please replace the paragraph starting at page 15, line 10 with the following paragraph.

A fifteenth pin 15p is a ground terminal of an external FET drive circuit. A sixteenth pin 16p is a terminal to be used for outputting a gate drive signal N1 of the NMOS ~~102~~ 101. A seventeenth pin 17p is a terminal to be used for outputting a gate drive signal N2 of the NMS ~~104~~ 102. An eighteenth pin 18p is a terminal to be used for outputting a gate drive signal P2 of the PMOS ~~103~~ 104. A nineteenth pin 19p is a terminal to be used for outputting a gate drive signal P1 of the PMOS ~~101~~ 103. A twentieth pin 20p is a power terminal into which a power supply voltage VCC is input.

Please replace the paragraph starting at page 16, line 19 with the following paragraph.

A PWM control signal or the like is input to the logic block 203, and a switch drive signal is produced in accordance with a predetermined logic. Gate drive signals P1, P2, N1, and N2 are applied to gates of the PMOSs ~~101, 103~~ 103, 104 and gates of the NMOSs ~~102, 104~~ 101, 102 by way of the output block 204.